

## PAYAL AGRAWAL

Address- MIG-2 C/97  
Dhanwantari Nagar,  
Jabalpur,  
[payalagrawal142@gmail.com](mailto:payalagrawal142@gmail.com)

Mobile No- +91-9479307417

Pin- 482003

### EDUCATION

<b>Indian Institute of Information Technology (IIITDM) (M.P.)</b>	<b>Jabalpur</b>
<i>Master of Technology in Electronics and Communication Engineering</i>	2016-2018
<ul style="list-style-type: none"><li>Specialization- Micro-nano Electronics (VLSI)</li><li>CGPA - 7.9/10</li></ul>	
<b>Rajiv Gandhi Prodyogiki Vishwavidhyalaya (RGPV)</b>	<b>Bhopal (M.P.)</b>
<i>Bachelor of Engineering in Electrical Engineering</i>	2009- 2013
<ul style="list-style-type: none"><li>Percentage- 76.25%</li></ul>	

### EXPERIENCE

<b>Department of Electronics and Communication Engineering, IIITDM</b>	<b>Jabalpur (M.P.)</b>
<i>Project Assistant</i>	1 MOS
<ul style="list-style-type: none"><li>Worked on Low Drop out regulator, voltage reference circuit (Power management unit )</li></ul>	
<b>Department of Electronics and Communication Engineering, IIITDM</b>	<b>Jabalpur (M.P.)</b>
<i>Teaching Assistant</i>	2016- 2018
<ul style="list-style-type: none"><li>Assisted and explained B.Tech students in working and running of lab instruments and kit of EDC lab, Basic electronics lab and Microprocessor lab.</li><li>Delivered tutorials fundamentals of basic electronics, microprocessor, EDC.</li></ul>	

### PROJECTS

- Thesis:**  
**Designed and analyzed Analog Multiplier (Gilbert cell Mixer) for wireless sensor application.**  
*Description-* Designed by using proposed Cascaded Current bleeding topology for increasing conversion gain and reducing noise figure.  
*Technology-* 180nm (RF Circuit design)
- Designed and analysed Gilbert cell mixer in RF integrated circuit design.**  
*Description-* Designed by using different existing topology and achieved performance in terms of conversion gain, noise figure, linearity, power consumption and load requirement.  
*Technology-* 180nm
- Designed Single Stage amplifier on Cadence Spectre.**  
*Description-* Common source, source follower, common gate and analyze the existing trade-off among speed, power and gain, Differential amplifier using different topology, Current mirror, Inverter.  
*Technology-* 180nm, 130nm
- Designed automatic solar tracking system for solar panel by using ATMEGA 8 microcontroller.
- Energy saving system for Cooler based on humidity sensor and temperature sensor.

### TRAINING AND CERTIFICATION

- Training on power system components from 220kv Substation, MPPTCL, Nayagaon , Jabalpur (M.P.)
- Training on air-conditioned coach from West Central Railway Zone, Jabalpur(M.P.)
- Training on grid system based on SCADA from SLDC, MPPTCL, Nayagaon , Jabalpur (M.P.)
- Training in MATLAB from CMC Ltd., Jabalpur (M.P.)
- Hands on training on designing, assembling of basic electronics circuits from Shivam technologies, Jab(M.P.)

### SCHOLARSHIPS AND ACHIEVEMENTS

MHRD Scholarship (2016-2018); TFW Scholarship (2009-2013); Ranked 2<sup>nd</sup> in instant essay competition held in school  
Gate 2016 Score: 547 AIR:4430; Gate 2017:452 Maths mark: 12th- 95/100, 10th- 98/100

### SKILLS

EDA Tools: Cadence Virtuoso and Spectre, Mentor-graphics, Vivado, Silvaco  
Simulation tools : Proteus, MATLAB; Languages : C, Verilog