

PRATEEK JAIN

B.E in Electrical Engineering.
M.E. in Electrical Machines and Drives

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Objective

“To channelize my knowledge and enthusiasm in achieving higher decision-making roles.”

Academic Qualification

Course	Institute	University/Board	Year	Percentage
M.E.(E.M.D.) (Hons.)	S.A.T.I., VIDISHA	S.A.T.I., VIDISHA	2014-2016	8.51(CGPA)
B.E. (EE) (Hons.)	S.A.T.I., VIDISHA	RGPV Bhopal	2009-2013	75.85%
S.S.C	Saraswati Vidhya Mandir, VIDISHA	M.P. BOARD	2009	90.60%
H.S.C	Saraswati Vidhya Mandir, VIDISHA	M.P. BOARD	2007	83.20%

PROFESSIONAL EXPERIENCE

Assistant System Engineer, Tata Consultancy Service (TCS), Noida, India

Dec '2016 – Oct '2017

- Trained exclusively on AS400 and Synon Tool along with case studies based on them.
- Collaborated with a team which dealt in designing, developing and testing of several functionalities/applications using Synon for US based CVS Pharmacy Benefit Manager.
- Screen development for insurance claim setups using AS400.
- Based on my abilities and reviews from my supervisors, I was tasked to manage a solo project.

Worked as an Assistant System Engineer Trainee, TCS, Trivandrum, India

Oct' 2016 – Dec' 2016

- Basic training on As400 operating system and Synon tool by IBM.
- Development of application functionalities and screens as a part of learning for CVS Health Pharmacy Benefit Manager(PBM) using DB2, CLLE and RPGLE
- Case study on development of Automated Teller Machine(ATM) using Synon.

Faculty, Samrat Ashok Technological Institute, Vidisha

Jul '16– Sept '16

- Taught Microprocessor, Basic Assembly Language, and Network analysis to the final semester master's students.

Faculty, Competitive Exams

Apr'18- present

- Teaching Hindi for Various Competitive Exams of Vyapam. Specially for MPPSC(Mains) , MPSI with question solving techniques with high demand of subject.

ACADEMIC PROJECTS

Final Thesis, (M.E.) S.A.T.I., Vidisha

Jan '16 – jul '16

'Harmonics Reduction of 3 Phase Diode Rectifier by Implementing P-Q Theory with Active Filter' starter

- Researched in detail, the cause and effect of harmonics in power system and studied the different types of proposed filters which are used to eliminate harmonics from the power system.
- Implemented reactive power control strategies using MATLAB for the modeling of - 3 Phase Diode Rectifier with SAPF and 3 Phase Diode Rectifier with shunt active power filter with current control.
- Compared the results obtained after simulation by FFT analysis (important for harmonic behavioral analysis) for harmonic elimination in the source current of the rectifier.

Pre-Dissertation, (M.E.) S.A.T.I., Vidisha

Jul '15 – Dec '15

'Case study of Akagi Theory'

- Researched in detail the 'p-q' Theory or the instantaneous active and reactive power theory which is widely used to design active filters controllers.
- Source: - Joao Afonso, Carlos Couto, Julio Martins, Active filters with control based on the p-q Theory, IEEE Industrial Electronics Society Newsletter vol.47, Sept. 2000, ISSN:0746-1240, PP. 5-10.
- Study of various International standards concerning electrical power quality IEEE-519, EN 50160, IEC 61000.

Three phase induction motor starting by STAR-DELTA starter, (B.E.) S.A.T.I., Vidisha Jan '13 – May '13

- Designed a Starter to start 3 phase induction motor. This reduced the starting current drawn by the motor.
- The motor is started in STAR mode. After the motor has gained sufficient speed, the connections were changed to DELTA mode to allow the motor to run at its full speed and torque.

PUBLICATION

"Harmonics Reduction of 3 phase diode bridge rectifier by implementing P-Q theory with active filter" IJSRD - International Journal for Scientific Research & Development| Vol. 4, Issue 07, 2016 | ISSN (online): 2321-0613.

TRAININGS

- Initial Learning Program (ILP) at Tata Consultancy Service (TCS) on AS400 and Synon.
- Completed a 15-day training program, in B.H.E.L. Bhopal, on traction motors and transformers. Understood the construction of various parts of electrical machines and control circuit layout diagrams.
- Studied the power distribution and transmission, and complete layout of the distribution system at a 15-day training period in MPMKVCL.

SKILLS

- Programming Languages : C, RPGLE, CLLE, ASSEMBLY.
- Design and Simulation Software:- Matlab
- Microcontrollers :- 8051,8085,8086 and basic of Aurdino.
- Tools used :- Synon.
- Operating System :- AS400 , windows.
- Database :- DB2.

EXTRA-CURRICULAR ACTIVITIES

- GATE qualified in 2013, 2014, 2015,2016.
- All India Jain Talent Award in 2009.
- Vidhya Sagar Pratibha Samman in 2005, 2007 & 2009.
- Certificate of excellence in “NATIONAL CREATIVE APTITUDE TEST” in 2012.
- Secured 2nd position in Group Dance competition at college level.
- Active participant in various competition at college and school level.

Declaration

I HEREBY declare that all the particulars stated in this resume are true to the best of my knowledge and belief. I have not concealed any information.

Date:

Place: VIDISHA

PRAITEEK JAIN